



1	Subclass
	Class
ISSUE CLASSIFICATION	

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PATENT NUMBER

U.S. UTILITY Patent Application

SCANNED	O.I.P.E. <i>KW</i>	PATENT DATE
	Q.A. <i>A.G.</i>	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/836065		<i>354</i> <i>714</i>	<i>718</i>	<i>2058</i> <i>2133</i>	<i>Chaudry</i>

APPLICANTS

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TITLE

System and method for secure test of integrated circuit device
having non-homogeneously sized sectors

PTO-2040
12/89

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION							

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____					
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	(Primary Examiner) _____ (Date) _____			ISSUE FEE	
				Amount Due	Date Paid
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